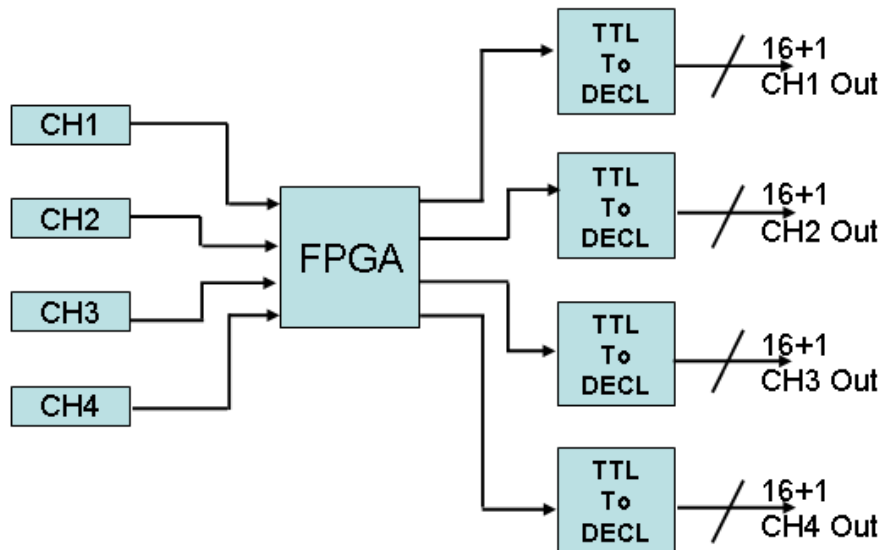


The DE4001 DSPBrik accepts data via the high-speed, high-density edge connector and outputs through 4 separate SCSI+ .050 style 50-pin connectors.



Features

- 4-Channel, Differential ECL (DECL) DSPBrik™ output module
- 4 independent DECL output channels with 16 data bits and 1 clock signal
- Onboard FPGA to format the data for output to other processing systems

Description

The Rincon Research Corporation Model DE4001 is a 4-Channel, Differential ECL (DECL) DSPBrik™ output module. The DE4001 accepts data via the high density DSPBrik platform edge connector and provides four independent DECL output channels with 16 data bits and 1 clock signal. The DE4001 uses an onboard FPGA to format the data for output to other processing systems using Differential ECL (DECL) input signaling.

The DE4001 is powered via a 12VDC attached Point-of-Load (POL) DC/DC converter module. The DC/DC POL module provides the required 5VDC, 3.3VDC, and 1.5VDC. The -5VDC is provided by a PCB mounted isolated DC/DC converter module.





Specifications

General

FPGA Size	0.5 million gates
Power Connector	Molex 3-Pin Mini-FitJR®

Input

Edge Connectors	1
Signal Level	LVCMOS (3.3V)
Number of Ports	4 per edge connector
Data Rate	100 MSPS (max.)
Width	16 data bits per port (single-ended) +1 Clk Pair per port (differential)
Style	QSH-090

Output

Connectors	4
Signal Level	Negative Differential ECL (DECL)
Data Rate	100 MSPS (max.)
Width	16+1 Differential Pairs/Connector
Style	SCSI+ .050 50-Pin Connector

Physical Properties

Dimensions	3-7/8"W x 3-7/8"D x 1-3/4"H (Edge connectors add 3/16" to W and D)
Temperature, Operating	0C to +50C
Power (Standard Configuration)	10–13VDC