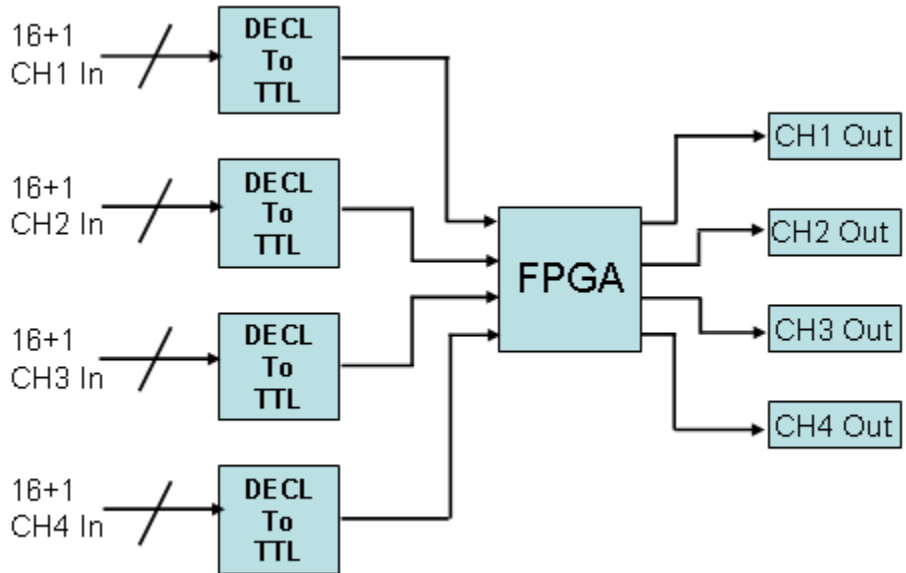
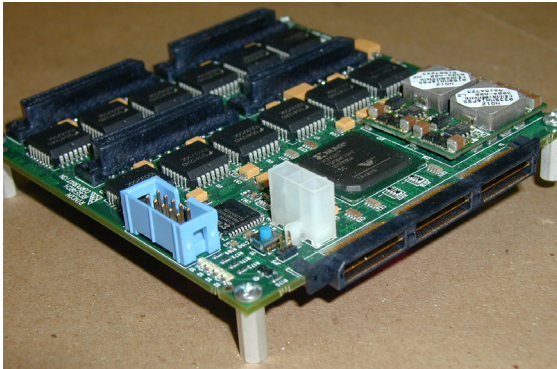


*The DSPBrik™  
DE4002 Input Module  
accepts up to four  
channels of  
Differential ECL data  
up to 100 Mega-  
Samples-per-Second  
(MSPS)*



## Features

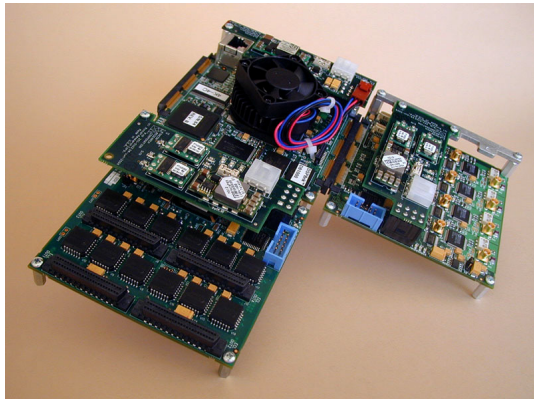
- 4 Channel Differential ECL (DECL) DSPBrik input module
- 4 Independent DECL input channels with 16 data bits and 1 clock signal
- Onboard FPGA to format data for input to other DSPBrik modules



## Description

Rincon Research Corporation's Model DE4002 DSPBrik™ converts up to four 16-bit Differential ECL (DECL) input channels via the four SCSI+ .050 50-pin connectors into the standard DSPBrik I/O connector format for output via the high-speed edge connector. Each input channel is individually passed through the FPGA and output via the high-speed edge connector.

The DE4002 is powered via a 12VDC nominal input and all DC/DC conversion accomplished on the module.



## Specifications

### General

FPGA Size	0.5 Million Gates
Power Connector	Molex 3-pin Mini-FitJR®

### Input

Connectors	4
Data	16-bit Differential
Clock	One Differential Pair per Channel
Data Rate	100 MSPS (max.)
Signal Level	Negative Differential ECL (DECL)
Style	SCSI+ .050 50-Pin Connector

### Output

Edge Connector(s)	1
Number of Ports	4 per edge connector
Data	16-bit (single ended)
Clock	1 LVDS pair per port
Data Rate	100 MSPS (max.)
Signal Level	LVC MOS (3.3V)
Style	QTH-090

### Physical Properties

Dimensions	3-7/8"W x 3-7/8"H x 1-3/4"D Edge connectors add 3/16" to H and W
Temperature, Operating	0–+50C
Power	10–13 VDC