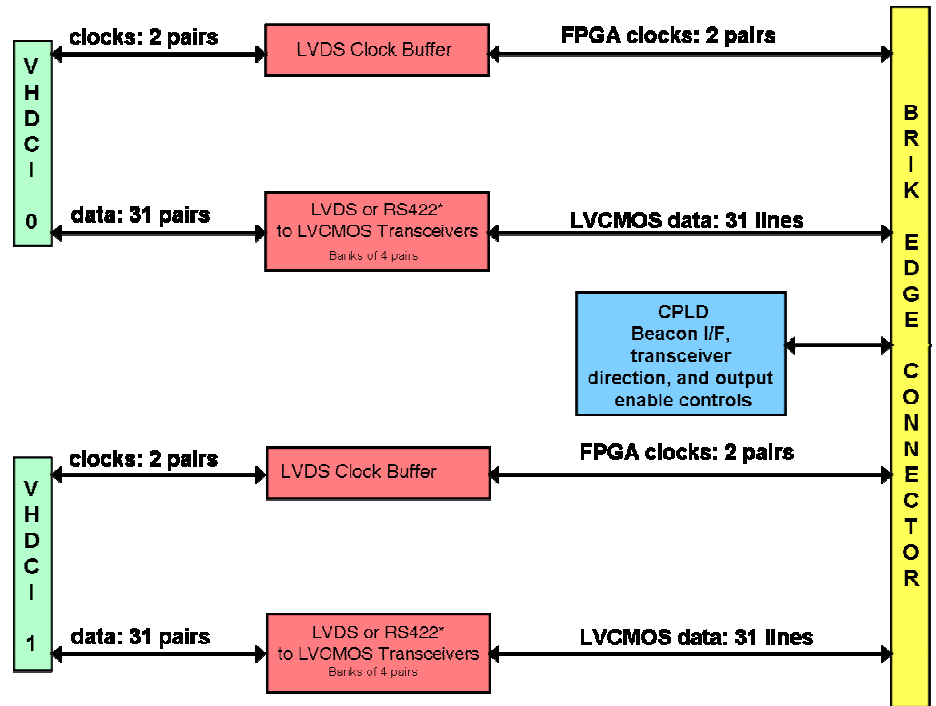


# DSPBrik™ LVDS Interface Brik DL2001 and DL2002

*The DL2001 DSPBrik is a bi-directional LVDS interface Brik. It enables a host Brik to transmit and receive differential signals across two cables.*



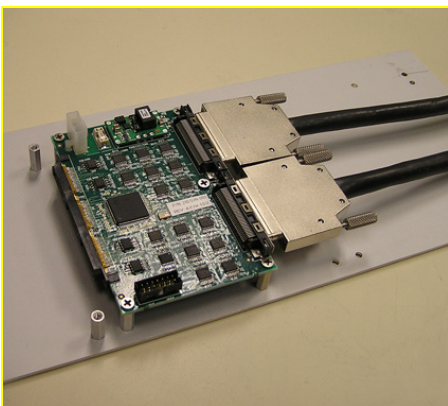
## Features

- Two 68-pin VHDCI (SCSI-3) connectors
- 31 LVDS pairs per VHDCI connector
- 2 LVDS clock pairs per VHDCI connector
- Data direction controlled in groups of four pairs
- Clock directions controlled independently
- All direction control lines routed to Brik edge connector
- Greater than 150 Mbit/s data rates
- LVDS or RS422 must be specified at time of order

## Description

Rincon Research Corporation's Model DL2001 interfaces between an LVCMOS Brik edge connector and two LVDS cables. The direction of the data bits can be controlled in groups of four. On-board termination resistors protect the signal integrity of received data. Maximum data rates are dependent on cable length, but may exceed 150Mbits/s per pair.

The DL2001 has a compact, one-half Brik form factor. The DL2002 DSPBrik provides similar functionality in the same form factor with one cable connector for RS-422 data and one for LVDS data.



## Specifications

### LVC MOS Input/Output

Connector	QTH edge connector
I/O Groups	4
I/O Clocks	4

### High-Speed LVDS or RS422 Input/Output

Cable Connectors	Two 68-pin VHDCI
Data Bits (pairs) per cable	31
Clocks (pairs) per cable	2
Data rate	>150Mbit/s, per pair

### Physical Properties

Dimensions	4-7/16"W x 3-1/4"D x 1"H
Operating Voltage	10–13 VDC
Power Connector	Mini-Fit Jr.
Temperature, Operating	0C to +50C